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openlab and Intel continue to deliver training to programmers

CERN openlab has been teaming up with Intel to organize regular training for CERN's programmers. Even though the schedule of four regular workshops on two major computing topics was established more than two years ago, these sessions are usually over-subscribed.

Thanks to openlab's collaboration with Intel and Hewlett-Packard, each participant at these workshops has the comfort of working on a dedicated 8-core machine with a suite of tools, including Intel Software tools such as compilers and multi-threading optimizers.

About the workshops

- The "Multi-threading and Parallelism" workshop aims to prepare the attendees for the multi-core future.
- The "Computer Architecture and Performance Tuning" workshop teaches programmers how to write efficient code for today's computing cores based on the Intel Architecture.

Both workshops are held twice a year, last for two days each and are free of charge, with registration on a first-come first-served basis – see the links below for more details.

The most recent openlab workshop was on Computer Architecture and Performance Tuning, held on 6–7 October at CERN. openlab lecturers Sverre Jarpe and Andrzej Nowak, assisted by Jeff Arnold from Intel, covered topics such as computer architecture, benchmarking, optimization and compilers, plus high-level issues seen in specific programming languages.

In addition, Intel and openlab recently organized a special two-day workshop for CERN's key developers in mid-September. This event was focused on getting the best performance out of applications and hardware by using tools such as the Intel Performance Tuning Utility. Another event of this type is foreseen for late November and is again targeting an expert audience.

Useful links

CERN Training Catalogue: <http://cta.cern.ch/cta2/f?p=110:9> (<http://cta.cern.ch/cta2/f?p=110:9>)

openlab events site: <http://cern.ch/openlab-events> (<http://cern.ch/openlab-events>)

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